



COURSE OUTLINE

ECE 3rd Semester Autumn Semester 2017-18

Sl. No.	Course Code	Credits	Course Name	Name of Faculty	Abbreviation Used	Department
1.	MA-301	3	Probability and Statistics	Rita Rana	RIR	Applied Science & Humanities
2.	HS – 305	3	Industrial Economics and Management	Sandeep Chaudhary	SAC	Applied Science & Humanities
3.	EC-301	3	Analog Electronics	Ankit Sharma	ASH	Electronics & Communication Engg.
4.	EC-302	4	Digital Electronics	Vivek Mankotia	VIM	Electronics & Communication Engg.
5.	EC-303	3	Network Analysis & Synthesis	Munish Bhardwaj	MUB	Electronics & Communication Engg.
6.	EC-304	4	Signals & Systems	Manjeet Singh	MSI	Electronics & Communication Engg.
7.	EC-305	1	Analog Electronics Lab	Ankit Sharma	ASH	Electronics & Communication Engg.
8.	EC-306	1	Digital Electronics Lab	Vivek Mankotia	VIM	Electronics & Communication Engg.
9.	EC-307	2	MATLAB & its application in signals & systems	Manjeet Singh	MSI	Electronics & Communication Engg.



Jawaharlal Nehru Government Engineering College, Sundernagar
B.TECH-3rd Semester, Electronics & Communication Engineering
TIME-TABLE (For Autumn Semester 2017-2018)

Room No: 505

SEM: 3rd ECE

With effect from- 03/08/2017

Period/Day	1 st period 9:00-10:00am	2 nd period 10:00-11:00am	3 rd period 11:00-12:00noon	12:00-01:00pm	4 th period 01:00-02:00pm	5 th period 02:00-03:00pm	6 th period 03:00-04:00pm
Monday	HS-305(L) SAC	EC-304(L) MSI	EC-302(L) VIM	LUNCH	Texas Instruments University Program (TIUP)/Spoken Tutorial/ ISTE & IETE Activities		
Tuesday	EC-302(L) VIM	EC-304(L) MSI	EC-301(L) ASH		EC-303(L) MUB	HS-305(L) SAC	TIUP /Spoken Tutorial/ ISTE & IETE Activities
Wednesday	EC-303(L) MUB	EC-301(L) ASH	MA-301(T) RIR		EC-306 VIM DIGITAL LAB		TIUP /Spoken Tutorial/ ISTE & IETE Activities
			EC-304(T) MSI 506		EC-301(T) ASH	Spoken Tut./ISTE/IETE	
Thursday	EC-303(L) MUB	EC-304(L) MSI	EC-301(L) ASH		EC-307 MSI SIMULATION LAB		
Friday	EC-304(T) MSI	EC-303(T) MUB	EC-302(L) VIM		MA-301(T) RIR	EC-303(T) MUB	MA-301(T) RIR
	EC-305 ASH BASIC ELETX. LAB				EC-305 ASH BASIC ELETX. LAB		MA-301(L) RIR
Saturday	EC-302(T) VIM	EC-301(T) ASH	Spoken Tut./ISTE/IETE		EC-306 VIM DIGITAL LAB		MA-301(T) RIR
	EC-307 MSI SIMULATION LAB			MA-301(L) RIR	HS-305(L) SAC	EC-302(T) VIM 502	

L=Lecture T=Tute

बेदत्रयी साहा
Member Time Table Committee

अंकुश कपूर
Incharge Time Table Committee



COURSE OUTLINE

ECE 5th Semester Autumn Semester 2017-18

Sl. No.	Course Code	Credits	Course Name	Name of Faculty	Abbreviation Used	Department
1.	EC-501	4	Digital Communication	Ankush Kapoor	AKA	Electronics & Communication Engg.
2.	EC-502	4	Electromagnetic Field Theory	Manjeet Singh	MSI	Electronics & Communication Engg.
3.	EC-503	3	Electronic logic circuit design	Vivek Mankotia	VIM	Electronics & Communication Engg.
4.	EC-504	3	Electronic Measurements & Measuring Instruments	Ankush Kapoor	AKA	Electronics & Communication Engg.
5.	EC-505	3	Power Electronics	Manik Dogra	MAD	Electronics & Communication Engg.
6.	EC-506	3	Introduction to Microcontrollers for Embedded Systems	Ankit Sharma	ASH	Electronics & Communication Engg.
7.	ME-509	2	Automobile Technology	Aman Sharma	AMS	Mechanical Engineering
	TE-507		Geotextiles	Ankush Sharma	ANS	Textile Engineering
8.	EC-511	1	Power Electronics & EMMI Lab	Munish Bhardwaj	MUB	Electronics & Communication Engg.
9.	EC-512	1	Introduction to Microcontrollers for Embedded systems lab	Vivek Mankotia	VIM	Electronics & Communication Engg.
10.	EC-513	2	MATLAB and its application in communication systems	Ankush Kapoor	AKA	Electronics & Communication Engg.



Jawaharlal Nehru Government Engineering College, Sundernagar
B.TECH-5th Semester, Electronics & Communication Engineering
TIME-TABLE (For Autumn Semester 2017-2018)

Room No: 501

SEM: 5th ECE

With effect from- 03/08/2017

Period/Day	1 st period 9:00-10:00am	2 nd period 10:00-11:00am	3 rd period 11:00-12:00noon	4 th period 12:00-01:00pm	01:00-02:00pm	5 th period 02:00-03:00pm	6 th period 03:00-04:00pm
Monday	EC-506(L) ASH	EC-501(L) AKA	TIUP/Spoken Tutorial/ISTE & IETE	EC-502(T) MSI	LUNCH	EC-504(L) AKA	EC-502(L) MSI
			EC-511 MUB ELECTRICAL LAB				
Tuesday	EC-506(L) ASH	EC-513 AKA SIMULATION LAB				TIUP/Spoken Tutorial/ISTE & IETE	ME-509 AMS 501/ TE-507 ANS 504
		EC-512 VIM MICROCONTROLLER LAB	EC-502(T) MSI	EC-505(T) MAD			
Wednesday	EC-502(L) MSI	EC-511 MUB ELECTRICAL LAB		EC-505(T) MAD		EC-501(T) AKA	Texas Instruments University Program/Spoken Tutorial
		EC-513 AKA SIMULATION LAB					
Thursday	EC-506(L) ASH	EC-501(L) AKA	EC-503(L) VIM	EC-504(L) AKA			EC-512 VIM MICROCONTROLLER LAB
Friday	EC-503(L) VIM	EC-501(L) AKA	EC-502(L) MSI	EC-504(L) AKA		EC-505 (T) MAD	TIUP/Spoken Tutorial/ISTE & IETE
					EC-505(L) MAD	ME-509 AMS 405/ TE-507 ANS 504	
Saturday	EC-505(L) MAD	EC-505(T) MAD	EC-503(L) VIM	TIUP/Spoken Tutorial/ISTE & IETE		Texas Instruments University Program(TIUP)/Spoken Tutorial/Library/Technical & Cultural Activities	
		TIUP/Spoken Tutorial/ISTE & IETE					

L=Lecture T=Tute

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अंकुश कपूर
Incharge Time Table Committee



COURSE OUTLINE

ECE 7th Semester Autumn Semester 2017-18

Sl. No.	Course Code	Course Name	Name of Faculty	Abbreviation Used	Department
1.	EC-411(b)	Biomedical Electronics	Nitasha Bisht	NIB	Electronics & Communication Engg.
2.	EC-412	Digital System Design using HDL	Vicky Kumar	VIK	Electronics & Communication Engg.
3.	EC-413	Digital Signal Processing	Munish Bhardwaj	MUB	Electronics & Communication Engg.
4.	EC -414	Wireless & Mobile Communication	Vicky Kumar	VIK	Electronics & Communication Engg.
5.	EC -415	CMOS & VLSI Design	Pooja Sharma	PSH	Electronics & Communication Engg.
6.	EC-416(P)	VLSI & HDL Lab	Vicky Kumar	VIK	Electronics & Communication Engg.
7.	EC-413(P)	Digital Signal Processing Lab	Munish Bhardwaj	MUB	Electronics & Communication Engg.
8.	EC-414(P)	Wireless Communication Lab	Vicky Kumar	VIK	Electronics & Communication Engg.
9.	EC-497	Seminar	Ankit Sharma	ASH	Electronics & Communication Engg.
10.	EC-498	Project-I	NIB,AKA,ASH,VIM,MSI,MUB		Each student has to attend 6 hours with respective guides depending upon the availability of guides from respective slots and may schedule extra time either lunch or from 4 to 5 P.M. in case of non-availability.



Jawaharlal Nehru Government Engineering College, Sundernagar
B.TECH-7th Semester, Electronics & Communication Engineering
TIME-TABLE (For Autumn Semester 2017-2018)

Room No: 501,505

SEM: 7th ECE

With effect from- 03/08/2017

Period/Day	1 st period 9:00-10:00am	2 nd period 10:00-11:00am	3 rd period 11:00-12:00noon	4 th period 12:00-01:00pm	01:00-02:00pm	5 th period 02:00-03:00pm	6 th period 03:00-04:00pm
Monday	EC-413(P) MUB SIMULATION LAB		EC-411b (L) NIB 501	EC-412(L) VIK 505	LUNCH	EC-415(L) PSH 505	EC-413(L) MUB 505
	EC-412(T) VIK Processor lab	EC-414(T) VIK Processor lab					
Tuesday	EC-412(T) VIK 402	EC-413(L) MUB 501	EC-414 (L) VIK 501	LUNCH	EC-415(L) PSH 501	*EC-498 PROJECT-1 (EC,MP LAB)	
	EC-411 b(T) NIB 506					EC-497 ASH DIGITAL LAB	
Wednesday	EC-412(L) VIK 504	EC-411b (L) NIB 501	EC-414(L) VIK 501	LUNCH	*EC-498 PROJECT-1 (EE,EC LAB)		EC-415(T) PSH 505
					EC-414(P) VIK PROCESSOR LAB		EC-413(T) MUB 501
Thursday	EC-411 b(T) NIB 506	*EC-498 PROJECT-1 (EE,MP LAB)	EC-413(T) MUB PROCESSOR LAB	LUNCH	*EC-498 PROJECT-1 (EC,EE,MP,DE LAB)	EC-411b (L) NIB 401	*EC-498 PROJECT-1 (EC,EE,DE LAB)
	EC-416(P) VIK SIMULATION LAB						
Friday	EC-416(P) VIK SIMULATION LAB			EC-413(L) MUB 505	LUNCH	EC-412(L) VIK 505	EC-414(T) VIK 501
	EC-415(T) PSH 506	*EC-498 PROJECT-1 (DE,MP LAB)					*EC-498 PROJECT-1 (EC,MP,DE LAB)
Saturday	EC-414 (P) VIK PROCESSOR LAB		EC-414(L) VIK 505	LUNCH	EC-415(L) PSH 501	EC-497 ASH DIGITAL LAB	
	*EC-498 PROJECT-1 (EE,EC,MP, DE LAB)					EC-413(P) MUB SIMULATION LAB	

L=Lecture T=Tute P=Practical

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