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| **institute** | **JAWAHARLAL NEHRU GOVERNMENT ENGINEERING COLLEGE SUNDERNAGAR, DISTT. MANDI (HP)** |

**INVITATION LETTER**

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| **Package Code: TEQIP-III/2019/HP/jnge/117** | **Current Date:** |
| **Package Name: JNGEC/LIBRARY/BOOKS ECE/2019** | **Method: Shopping Goods** |

**Sub: INVITATION LETTER FOR JNGEC/LIBRARY/BOOKS ECE/2019**

Dear Sir,

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| **1.** | You are invited to submit your most competitive quotation for the following goods with item wise detailed specifications given at Annexure I, |

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| --- | --- | --- | --- | --- |
| **Sr. No** | **Item Name** | **Quantity** | **Place of Delivery** | **Installation Requirement (if any)** |
| 1 | Library books related ECE Engg. | 238 | JNGEC Sundernagar | NA |

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| **2.** | | Government of India has received a credit from the International Development Association (IDA) towards the cost of the **Technical Education Quality Improvement Programme [TEQIP]-Phase III** Project and intends to apply part of the proceeds of this credit to eligible payments under the contract for which this invitation for quotations is issued. | | | | | | | | |
| **3.** | | **Quotation** | | | | | | | | |
|  | | 3.1 | | | | | | The contract shall be for the full quantity as described above. | | |
|  | | 3.2 | | | | | | Corrections, if any, shall be made by crossing out, initialling, dating and re writing. | | |
|  | | 3.3 | | | | | | All duties and other levies payable by the supplier under the contract shall be included in the unit Price. | | |
|  | | 3.4 | | | | | | Applicable taxes shall be quoted separately for all items. | | |
|  | | 3.5 | | | | | | The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account. | | |
|  | | 3.6 | | | | | | The Prices should be quoted in Indian Rupees only. | | |
| **4.** | | Each bidder shall submit only one quotation. | | | | | | | | |
| **5.** | | Quotation shall remain valid for a period not less than **90**days after the last date of quotation submission. | | | | | | | | |
| **6.** | | Evaluation of Quotations: The Purchaser will evaluate and compare the quotations determined to be Substantially responsive i.e. which | | | | | | | | |
|  | | 6.1 | | | | | are properly signed; and | | | |
|  | | 6.2 | | | | | Confirm to the terms and conditions, and specifications. | | | |
| **7.** | | The Quotations would be evaluated for all items together. | | | | | | | | |
| **8.** | | Award of contract The Purchaser will award the contract to the bidder whose quotation has been determined to be substantially responsive and who has offered the lowest evaluated quotation price. | | | | | | | | |
|  | | 8.1 | | | | | Notwithstanding the above, the Purchaser reserves the right to accept or reject any quotations and to cancel the bidding process and reject all quotations at any time prior to the award of Contract. | | | |
|  | | 8.2 | | | | | The bidder whose bid is accepted will be notified of the award of contract by the Purchaser prior to expiration of the quotation validity period. The terms of the accepted offer shall be Incorporated in the purchase order. | | | |
| **9.** | | Payment shall be made in Indian Rupees as follows: | | | | | | | | |
|  |  | | |  | | **Delivery and Acceptance - 100% of total cost** | | | | | |
| **10.** | | | Liquidated Damages (L.D):  If a supplier fails to execute the order in time as per the terms and conditions stipulated therein, it will be open to the purchaser to recover liquidated damages for delay in delivery and installation from the supplier at the rate 0.5% of the value of the order per week subject to a maximum of 10% of the total order value. The L.D charges can be increased in case of gross violation of the Purchase Order terms as decided by the Principal of the Institute. | | | | | | | |
| **11.** | | | All supplied items are under warranty of **36** months from the date of successful acceptance of items and AMC/Others is **N.A.** | | | | | | | |
| **12.** | | | You are requested to provide your offer latest by **12:00** hours on **16-Jan-2020.** | | | | | | | |
| **13.** | | | Detailed specifications of the items are at Annexure I. | | | | | | | |
| **14.** | | | Training Clause (if any) **nil** | | | | | | | |
| **15.** | | | Testing/Installation Clause (if any) **nil** | | | | | | | |
| **16.** | | | Tenderer or his/her authorized representative (with proper authorization letter for opening of technical bids and also for opening of price bids) may choose to be present at the time of opening of Technical bids/Price Bids. | | | | | | | |
| **17.** | | | | Information brochures/Product catalogue must be accompanied with the quotation clearly indicating the model quoted for. | | | | |
| **18.** | | | | Sealed quotation to be submitted/ delivered at the address mentioned below, **Jawaharlal Nehru Government Engineering College Sundernagar, DISTT. MANDI (HP).** | | | | |
| **19.** | | | | The offer/bid should be submitted in one bid system as per the format mentioned in Annexure- II. | | | | |
| **20.** | | | | Bid Format: The bid comprising of following documents should be sealed in envelope mentioning **Bid for “Package Name”** on top of the envelope. The envelope must mention package no. and name. | | | | |
| **21.** | | | | Bid must include: 1.) A compliance list against the technical specifications as per Annexure-I should be provided.  2.) The Bid should be submitted as per the bid format. (Annexure-II)  3.) Proof of Registration of the bidder.  4.) Copies of PAN Card and GST registration no.  5.) Signed copy of the tender document, with company seal, agreeing to the terms & conditions must be submitted.  6.) It should indicate item wise price for the items mentioned in the bid as per the format (Annexure-III).  7.) The price quoted should be F.O.R JNGEC Sundernagar.  8.) All duties and other levies payable by the supplier under the contract shall be included in the unit price.  9.) The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.  10.) All duties and other levies payable by the supplier under the contract shall be included in the unit price.  11.) The prices quoted by the bidder shall be fixed for the duration of the contract and shall not be subject to adjustment on any account.  12.) The Prices should be quoted in Indian Rupees only.  All documentary proof must be listed on the letterhead of the company. | | | | |
| **23.** | | | | **Opening of Bids:**  Bids will be opened on the last date of receipt of the quotations at 3P.M. in presence of Purchase Committee members and representatives of the vendors (if any). | | | | |
| **24.** | | | | Tenderer or his/her authorized representative (with proper authorization letter for opening of bids) may choose to be present at the time of opening of Bids. | | | | |
| **25.** | | | | **The Institute reserves the right to reject any bid not fulfilling the eligibility criteria** | | | | |
| **26.** | | | | **Supply of Items:** Free of cost at JNGEC Sundernagar by the vendor | | | | |
| **27.** | | | | **IMPORTANT:**   1. A bid submitted with false information will not only be rejected but also the OEM/vendor will be debarred from participation in future tendering process. 2. Each bidder shall submit only one quotation 3. In case of any dispute, the decision of the Principal of this Institute shall be final and binding on the bidders. 4. In case the due date for submission of the tender happens to be a holiday, the same will be accepted on the next working day. 5. The Authority of JNGEC Sundernagar reserves the right to reject any or the entire tender bids received without assigning any reason thereof. 6. Bidder should submit the tender document, duly signed and stamped on every page in token of accepted all the terms and conditions of the tender. 7. The institute does not bind itself to offer any explanation to those bidders whose bids have not been found acceptable by the evaluation committee of the institution. 8. The bids once submitted will be the property of the institute. 9. **All the books included in the package should be taken as a single unit and flat discount considering all books should be quoted.** 10. **At least 75% of the books included in the package need to be supplied and undertaking in this regard has to be given by the bidder during bid submission stage.** 11. **Proof of price shall be attached with the submitted bid which will justify M.R.P. of each and every single book in the package.** | | | | |
| **28.** | | | | **We look forward to receiving your quotation and thank you for your interest in this project.** | | | | |
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(Authorized Signatory)

DIRECTOR/PRINCIPAL

**Annexure-I**

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Srl No** | **Authors** | **Title** | **Publisher Name** | **Qty** | **Price** | **Amount** |
| 1 | Dr. P. Prasad | Universal English-[AICTE] | S.K Kataria and Sons | 10 |  |  |
| 2 | Dr. BhartiKukreja,Dr. Anupama Jain | Effective Technical Communication-[AICTE] | S.K Kataria and Sons | 10 |  |  |
| 3 | K.M. Soni | Network Theory (AICTE) | S.K Kataria and Sons | 10 |  |  |
| 4 | J.B. Gupta | An Integrated Course In Electronics & Communication Engg. | S.K Kataria and Sons | 10 |  |  |
| 5 | J.B.Gupta | Question Bank In Electronics And Communication Engg | S.K Kataria and Sons | 10 |  |  |
| 6 | Dr.Sanjay Sharma | Question Bank In Electronics And Communication Engg. | S.K Kataria and Sons | 10 |  |  |
| 7 | B.R. Gupta | Electronics And Communication Engg.Ob/Ty | S.K Kataria and Sons | 10 |  |  |
| 8 | J.B.Gupta | Gate Electronics And Communication Engineering | S.K Kataria and Sons | 10 |  |  |
| 9 | ALLEN | CMOS ANALOG CIRCUIT DESIGN, 3E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 10 | BELL | ELECTRONIC DEVICES AND CIRCUITS, 5E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 11 | BELL | ELECTRONIC INSTRUMENTATION AND MEASUREMENT, 3E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 12 | BELL | OPERATIONAL AMPLIFIERS and LINEAR ICs, 3E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 13 | BHADRA | WIND ELECTRICAL SYSTEMS | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 14 | BHALJA | PROTECTION AND SWITCHGEAR 2E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 15 | BHANOT | PROCESS CONTROL - PRINCIPLES AND APPLICATIONS | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 16 | BHATTACHARYA | SOLID STATE ELECTRONIC DEVICES, 2E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 17 | BHOOSHAN | FUNDAMENTALS OF ENGINEERING ELECTROMAGNETICS | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 18 | BOBROW | FOUNDATIONS OF ELECTRICAL ENGINEERING (ASIAN EDITION) | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 19 | CHANDRASEKHAR | ANALOG COMMUNICATION | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 20 | COMER | DIGITAL LOGIC AND STATE MACHINE DESIGN, 3E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 21 | DAS | MICROWAVE ENGINEERING | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 22 | DAS | VLSI DESIGN, 2E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 23 | DAS | VHDL: DESIGN, SYNTHESIS, AND SIMULATION | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 24 | DIMITRIJEV | SEMICONDUCTOR DEVICES, 2E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 25 | DUTTA | SEMICONDUCTOR DEVICES AND CIRCUITS | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 26 | GURU | ELECTRIC MACHINERY, 3E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 27 | HARISH | ANTENNAS AND WAVE PROPAGATION | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 28 | KEN MARTIN | DIGITAL INTEGRATED CIRCUIT DESIGN | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 29 | KHARATE | DIGITAL ELECTRONICS | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 30 | KHARE | FIBER OPTICS and OPTOELECTRONICS | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 31 | KREIN | ELEMENTS OF POWER ELECTRONICS 2E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 32 | KUO | DIGITAL CONTROL SYSTEMS, 2E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 33 | LATHI | PRINCIPLES OF LINEAR SYSTEMS AND SIGNALS, 2E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 34 | LATHI | MODERN DIGITAL AND ANALOG COMMUNICATION SYSTEMS 4E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 35 | LATHI | PRINCIPLES OF SIGNAL PROCESSING AND LINEAR SYSTEMS, (INTL VERSION) | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 36 | NAGSARKAR and SUKHIJA | POWER SYSTEM ANALYSIS, 2E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 37 | PURKAIT | ELECTRICAL MACHINES | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 38 | RAMAKALYAN | LINEAR CIRCUITS | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 39 | RAWAT | DIGITAL SIGNAL PROCESSING | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 40 | RISAL SINGH | INTRODUCTION TO NANOTECHNOLOGY | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 41 | SADIKU AND KULKARNI | PRINCIPLES OF ELECTROMAGNETICS, 6E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 42 | SALIVAHANAN | DIGITAL CIRCUITS AND DESIGN 5E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 43 | SCHAUMANN | ANALOG FILTER DESIGN, 2E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 44 | SEDRA | MICROELECTRONIC CIRCUITS: THEORY AND APPLICATIONS 7E (IA) | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 45 | STEFANI | DESIGN OF FEEDBACK CONTROL SYSTEMS, 4E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 46 | SUKHIJA | CIRCUITS AND NETWORKS, 2E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 47 | TSIVIDIS | OPERATION and MODELLING OF MOS TRANSISTOR, 3E | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 48 | RUDRA PRATAP | GETTING STARTED WITH MATLAB 7E | OXFORD UNIVERSITY PRESS | 10 |  |  |
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| 50 | SENTHIL KUMAR | MICROPROCESSORS AND INTERFACING | OXFORD UNIVERSITY PRESS | 5 |  |  |
| 51 | NAGSARKAR & SUKHIJA | PRINCIPLES OF BASIC ELECTRICAL ENGINNEERING (AICTE) | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 52 | NAGSARKAR and Sukhija | BASIC ELECTRICAL ENGINEERING, 3E | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 53 | NAGSARKAR and SUKHIJA | BASIC ELECTRICAL and ELECTRONICS ENGINEERING | OXFORD UNIVERSITY PRESS | 10 |  |  |
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| 55 | AUDRON RAKAUSKIEN, IRENA RAGAISIENE, RAMUTE ZEMAITIENE | ESSAY WRITING FOR EXAMS | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 56 | AUDRONE RASKAUSKIENE | LIGHTHOUSE IELTS GENERAL: PREPARATION AND PRACTICE | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 57 | AUDRONE RASKAUSKIENE AND SANTANU C | CHECKMATE: IELTS ACADEMIC PREP AND PRACTICE | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 58 | PHILIP SUNIL SOLOMON | WORD POWER FOR COMPETITIVE EXAMS | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 59 | SUNIL PHILIP SOLOMON | ENGLISH FOR SUCCESS IN COMPETITIVE EXAMS | OXFORD UNIVERSITY PRESS | 10 |  |  |
| 60 | Abid | Beginning with C11 &Matlab | Wiley | 10 |  |  |
| 61 | Alam | Understanding Matlab: A Textbook for Beginners | Wiley | 10 |  |  |
| 62 | Ananthasuresh | Mirco and Smart Systems: As per AICTE | Wiley | 10 |  |  |
| 63 | Apte | Advanced Digital Signal Processing: As per AICTE, w/cd | e | Wiley | 10 |  |  |
| 64 | Apte | Advanced Digital Signal Processing: As per AICTE, w/cd | e | Wiley | 10 |  |  |
| 65 | Apte | Digital Signal Processing, 2ed: As per AICTE , w/cd | e | Wiley | 10 |  |  |
| 66 | Apte | Speech and Audio Processing: As per AICTE | Wiley | 10 |  |  |
| 67 | Apte | The 8051 and MSP430 Microcontrollers: Architecture, Programming and Applications | Wiley | 10 |  |  |
| 68 | Arora | Fundamentals of High-Voltage Engineering | IM | Wiley | 10 |  |  |
| 69 | Bahadure | Microcontrollers and Embedded System Design | Wiley | 10 |  |  |
| 70 | Dorf | Dorf's Introduction to Electric Circuits, Wiley India Edition | Wiley | 10 |  |  |
| 71 | Kishore | VLSI Design | Wiley | 10 |  |  |
| 72 | Krishnaveni | Signals and Systems: As per AICTE | e | Wiley | 10 |  |  |
| 73 | Kulkarni | Basic Electronics | Wiley | 10 |  |  |
| 74 | Kumar | Engineering Economy and Management | Wiley | 10 |  |  |
| 75 | Maini | Digital Electronics: Principles and Integrated Circuits: As per AICTE | e | Wiley | 10 |  |  |
| 76 | Maini | Electronic Devices and Circuits, 2ed | Wiley | 10 |  |  |
| 77 | Maini | Satellite Communications: As per AICTE | e | Wiley | 10 |  |  |
| 78 | Maini | Wiley's GATE Electronics and Communication Engineering Chapter-wise Solved Papers (2000-2019) | Wiley | 10 |  |  |
| 79 | Maini | Wiley Acing the GATE: Electronics and Communication Engineering, 2ed, 2020 | Wiley | 10 |  |  |
| 80 | Maini | Wiley Acing the GATE: Engineering Mathematics and General Aptitude, 2ed, 2020 | Wiley | 10 |  |  |
| 81 | Mandal | Power Plant Engineering: Theory and Practice | Wiley | 10 |  |  |
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| 83 | Nair | Basic Electronics | Wiley | 10 |  |  |
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| 85 | Ramanujam | Computational Electromagnetic Transients | Wiley | 10 |  |  |
| 86 | Shah | Nanotechnology: The Science of Small , 2ed | Wiley | 10 |  |  |
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| 88 | Singh | Analog and Digital Electronics | Wiley | 10 |  |  |
| 89 | Sivanandam | MATLAB with Control System, Signal Processing and Image Processing Toolboxes, w/cd | e | Wiley | 10 |  |  |
| 90 | Varadarajan | Signals & Systems | Wiley | 10 |  |  |
| 91 | Sivanandam | Principles of Soft Computing, 3ed | IM | Wiley | 10 |  |  |
| 92 | Vasudevan | Internet of Things | IM | Wiley | 10 |  |  |
| 93 | Cogdell | Foundations of Electronics | Pearson | 5 |  |  |
| 94 | De | Basic Electronics | Pearson | 10 |  |  |
| 95 | Allen | Natural Language Understanding, 2e | Pearson | 5 |  |  |
| 96 | Haykin | Adaptive Filter Theory, 4e | Pearson | 5 |  |  |
| 97 | Jurafsky | Speech and Language Processing: An Introduction to Natural Language Processing, Computational Linguistics and Speech Recognition 2/e | Pearson | 5 |  |  |
| 98 | Quatieri | Discrete-Time Speech Signal Processing: Principles and Practice | Pearson | 5 |  |  |
| 99 | Rabiner | Digital Processing of Speech Signals | Pearson | 5 |  |  |
| 100 | Rabiner | Fundamentals Of Speech Recognition | Pearson | 5 |  |  |
| 101 | Widrow | Adaptive Signal Processing | Pearson | 5 |  |  |
| 102 | Fusco | Foundations of Antenna Theory and Techniques | Pearson | 10 |  |  |
| 103 | Raju | Antennas and Wave Propagation | Pearson | 10 |  |  |
| 104 | Bali | Consumer Electronics | Pearson | 5 |  |  |
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| 106 | Glover | Digital Communications, 2e | Pearson | 5 |  |  |
| 107 | Kundu | Analog and Digital Communications | Pearson | 5 |  |  |
| 108 | Proakis / Salehi | Communication Systems Engineering 2e | Pearson | 5 |  |  |
| 109 | Proakis | Fundamentals of Communication Systems | Pearson | 5 |  |  |
| 110 | Roddy | Electronic Communications, 4e | Pearson | 5 |  |  |
| 111 | Saha | Information Theory, Coding & Cryptography | Pearson | 5 |  |  |
| 112 | Sklar | Digital Communications, 2e | Pearson | 5 |  |  |
| 113 | Tomasi | Electronic Communications System: Fundamentals Through Advanced, 5e | Pearson | 5 |  |  |
| 114 | Tomasi | Advanced Electronic Communications Systems 6e | Pearson | 5 |  |  |
| 115 | Ciletti | Advanced Digital Design with the Verilog HDL, 2/e | Pearson | 5 |  |  |
| 116 | Fletcher | An Engineering Approach to Digital Design | Pearson | 5 |  |  |
| 117 | Floyd | Digital Fundamentals, 11e | Pearson | 5 |  |  |
| 118 | ITL ESL | Express Learning Series - Digital Electronics and Logic Design | Pearson | 5 |  |  |
| 119 | Johnson | High Speed Digital Design: A Handbook of Black Magic | Pearson | 5 |  |  |
| 120 | Kothari | Digital Circuits & Design 1/e | Pearson | 5 |  |  |
| 121 | Lincoln | Digital Electronics | Pearson | 5 |  |  |
| 122 | **Mano** | **Digital Design: With an Introduction to the Verilog HDL, VHDL, and System Verilog, 6e** | Pearson | 5 |  |  |
| 123 | Mano | Digital Logic & Computer Design | Pearson | 5 |  |  |
| 124 | Mano | Logic & Computer Design Fundamentals, 4e | Pearson | 5 |  |  |
| 125 | Rabaey / Chandrakasan / Nikolic | Digital Integrated Circuits 2e | Pearson | 5 |  |  |
| 126 | Tocci | Digital Systems: Principles and Applications, 10e | Pearson | 5 |  |  |
| 127 | Wakerly | Digital Design: Principles and Practices, 4e | Pearson | 5 |  |  |
| 128 | Rao | Switching Theory and Logic Design | Pearson | 5 |  |  |
| 129 | Annadurai | Fundamentals of Digital Image Processing | Pearson | 5 |  |  |
| 130 | Castleman | Digital Image Processing | Pearson | 5 |  |  |
| 131 | **Gonzalez** | **Digital Image Processing, 4e** | Pearson | 5 |  |  |
| 132 | Jain | Fundamentals of Digital Image Processing | Pearson | 5 |  |  |
| 133 | Ifeachor | Digital Signal Processing | Pearson | 5 |  |  |
| 134 | Johnson | Introduction to Digital Signal Processing, 1/e | Pearson | 5 |  |  |
| 135 | Oppenheim / Schafer | Digital Signal Processing | Pearson | 5 |  |  |
| 136 | Oppenheim | Discrete-Time Signal Processing, 3e | Pearson | 5 |  |  |
| 137 | Proakis | Digital Signal Processing, 4e | Pearson | 5 |  |  |
| 138 | Rabiner | Theory and Application of Digital Signal Processing, 1/e | Pearson | 5 |  |  |
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| 144 | Reitz | Foundations of Electromagnatic Theory | Pearson | 5 |  |  |
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| 148 | Cheruku | Electronic Devices and Circuits 2e | Pearson | 5 |  |  |
| 149 | Floyd | Electronic Devices 9e | Pearson | 5 |  |  |
| 150 | Floyd | Fundamentals of Analog Circuits 2e | Pearson | 5 |  |  |
| 151 | Hanson | Fundamentals of Nanoelectronics | Pearson | 5 |  |  |
| 152 | Horenstein | Microelectronic Circuit and Devices 2e | Pearson | 5 |  |  |
| 153 | Hu | Modern Semiconductor Devices for Integrated Circuits | Pearson | 5 |  |  |
| 154 | VisvesvaraRao | Electronic Devices and Circuits | Pearson | 5 |  |  |
| 155 | Ludwig | RF Circuit Design | Pearson | 5 |  |  |
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| 169 | Prince | Medical Imaging Signals and Systems | Pearson | 5 |  |  |
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| 172 | B. VisvesvaraRao | Linear Integrated Circuits | Pearson | 5 |  |  |
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| 174 | Stanley | Operational Amplifiers with Linear Integrated Circuits, 4e | Pearson | 5 |  |  |
| 175 | Barrett | Embedded Systems | Pearson | 5 |  |  |
| 176 | Das | Embedded Systems: An Integrated Approach | Pearson | 5 |  |  |
| 177 | Dave | Embedded Systems | Pearson | 5 |  |  |
| 178 | Ghoshal | 8051 Microcontroller: Internals, Instructions, Programming and Interfacing 2e | Pearson | 5 |  |  |
| 179 | Hackworth | Programmable Logic Controllers: Programming Methods and Applications | Pearson | 5 |  |  |
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| 233 | Trishna | Topic-wise Tests GATE Electronics and Communication Engineering | Pearson | 10 |  |  |
| 234 | Trishna's | GATE Electronics and Communication Engineering | Pearson | 10 |  |  |
| 235 | Rakhesh Singh Kshetrimayum | Fundamentals of MIMO Wireless Communications | Cambridge University Press; 1 edition (17 April 2017) | 5 |  |  |
| 236 | Salim Ali Jabbar | MIMO Fractal Antennas | LAP Lambert Academic Publishing | 5 |  |  |
| 237 | [Ramesh Garg](https://www.amazon.in/s/ref=dp_byline_sr_book_1?ie=UTF8&field-author=Ramesh+Garg&search-alias=stripbooks) | Microstrip Design Antenna Handbook | Artech House Publishers | 2 |  |  |
| 238 | Jonathan Rodriguez | Fundamentals Of 5G Mobile Networks | Wiley | 5 |  |  |

Signature of Librarian

Signature of Chairman Library

**Annexure II**

**TECHNICAL BID DOCUMENT**

**FORMAT TO BE FILLED BY OEM OR AUTHORISED VENDORSFOR SUBMITTING TENDER FOR PROCUREMENT OF \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

1. **Name of the Tenderer:**
2. **Details of the Address of the Vendors: (Attach Details)**
3. **Proof of Registration or Trade Licence: (Attach Details)**
4. **PAN and GST Registration No. (attach copies)**

**Certified that the above information are correct to the best of my /our information, knowledge and belief.**

**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**Dated Signature and seal of OEM/vendor**

**Annexure-III**

**FORMAT FOR QUOTATION SUBMISSION**

(In letterhead of the supplier with seal)

Date: \_\_\_\_\_\_\_\_\_\_\_\_\_

To:\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Sl. No. | Description of goods \ (with full Specifications) | Qty. | Unit | Quoted Unit rate in Rs.  (Including Ex-Factory price,  excise duty, packing and forwarding, transportation, insurance, other local costs incidental to delivery and  warranty/ guaranty commitments) | Total Price  (A) | Sales tax and other taxes payable | |
| In % | In figures (B) |
|  |  |  |  |  |  |  |  |
| **Total Cost** | | | | |  |  |  |

Gross Total Cost (A+B): Rs. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

We agree to supply the above goods in accordance with the technical specifications for a total contract price of Rs. ———————— (Amount in figures) (Rupees ————————amount in words) within the period specified in the Invitation for Quotations.

We confirm that the normal commercial warranty/ guarantee of ——————— months shall apply to the offered items and we also confirm to agree with terms and conditions as mentioned in the Invitation Letter.

We hereby certify that we have taken steps to ensure that no person acting for us or on our behalf will engage in bribery.

Signature of Supplier

Name: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Address: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Contact No. \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_